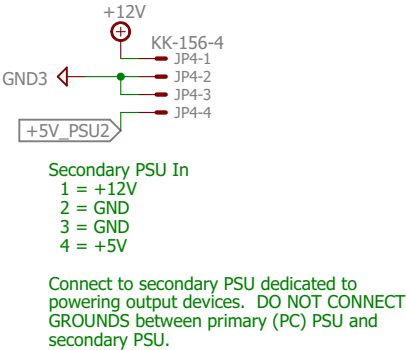
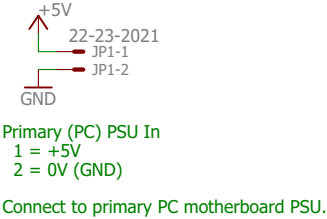


TLC5940 IN
1 = SIN
3 = SCLK
5 = XLAT
7 = BLANK
9 = GSCLK
2-4-6-8-10 = GND

TLC5940 OUT
1 = SOUT
3 = SCLK
5 = XLAT
7 = BLANK
9 = GSCLK
2-4-6-8-10 = GND



- NOTES:
- 100nF decoupling capacitor between TLC5940 Pins 21-22 should be as close to the IC leads as possible.
 - PSU2 is a separate PSU dedicated to feedback devices. DO NOT CONNECT GROUNDS of primary PC PSU and secondary PSU. Secondary PSU should be electrically isolated from PC PSU to minimize logic signal interference from electrical noise from inductive and high power devices.
 - Use a flyback diode (1N4007 or similar) on every inductive output device. Attach the diode close to the device's power terminals, with the "bar" side attached to the positive (+) power terminal.
 - Fuses are recommended for MOSFET outputs. Fuse each output individuall at approx 75% of MOSFET current limit.

PWM Array

TITLE: ps2

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